



Inventor Name Search Result

Your Search was:

Last Name = CHANG

First Name = CHING-YU

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09506391	6289605	150	02/18/2000	Method for drying a semiconductor wafer	CHANG, CHING-YU
09510294	Not Issued	161	02/22/2000	Apparatus for preventing the re-adherence of particles a in wafer-cleaning process	CHANG, CHING-YU
09682051	6479387	150	07/15/2001	METHOD FOR REDUCING MICRO-PARTICLE ADSORPTION EFFECTS	CHANG, CHING-YU
09682134	6423147	150	07/26/2001	METHOD OF CLEANING A WAFER	CHANG, CHING-YU
09682340	6461981	150	08/22/2001	METHOD OF FORMING A CONFORMAL OXIDE FILM	CHANG, CHING-YU
09682481	Not Issued	161	09/07/2001	Method of forming a via of a dual damascene with low resistance	CHANG, CHING-YU
09682808	Not Issued	161	10/22/2001	Rom code mask with assistant features	CHANG, CHING-YU
09695208	Not Issued	161	10/24/2000	Spinning etching machine for a wafer	CHANG, CHING-YU
09712916	6436612	150	11/16/2000	METHOD FOR FORMING A PROTECTION DEVICE WITH SLOPE LATERALS	CHANG, CHING-YU
09714195	Not Issued	161	11/17/2000	Method for forming lightly doped drains	CHANG, CHING-YU
09714200	6518125	150	11/17/2000	METHOD FOR FORMING FLASH MEMORY WITH HIGH COUPLING RATIO	CHANG, CHING-YU
09715655	Not Issued	161	11/17/2000	Apparatus and method for preventing the re-adherence of particles in wafer-cleaning process	CHANG, CHING-YU
09726459	6607674	150	11/30/2000	METHOD OF REPAIRING A PHASE SHIFTING MASK	CHANG, CHING-YU
09727125	Not Issued	161	11/30/2000	Vertical low-pressure chemical vapor deposition furnace	CHANG, CHING-YU
09727154	6576515	150	11/30/2000	METHOD OF FORMING TRANSISTOR GATE	CHANG, CHING-YU
09727946	Not Issued	94	12/01/2000	HOT PLATE COOLING SYSTEM	CHANG, CHING-YU

<u>09731753</u>	Not Issued	161	12/08/2000	Method for preventing Cu cross contamination	CHANG, CHING-YU
<u>09734285</u>	6399259	150	12/11/2000	METHOD OF FORMING ALIGNMENT MARKS FOR PHOTOLITHOGRAPHIC PROCESSING	CHANG, CHING-YU
<u>09734406</u>	6300196	150	12/11/2000	Method of fabricating gate	CHANG, CHING-YU
<u>09750081</u>	6300200	150	12/29/2000	DEVICE FABRICATION METHOD FOR NON-VOLATILE MEMORY DEVICE USED FOR NON-OVERLAPPING IMPLANT	CHANG, CHING-YU
<u>09752432</u>	Not Issued	161	01/03/2001	Method for forming a passivation on buried diffusion layer of a non-volatile memory	CHANG, CHING-YU
<u>09777877</u>	6489251	150	02/07/2001	METHOD OF FORMING A SLOPE LATERAL STRUCTURE	CHANG, CHING-YU
<u>09799003</u>	6562691	150	03/06/2001	METHOD FOR FORMING PROTRUSIVE ALIGNMENT-MARK	CHANG, CHING-YU
<u>09799004</u>	6555468	150	03/06/2001	METHOD FOR FORMING TRENCH INCLUDING A FIRST AND A SECOND LAYER OF PHOTORESIST	CHANG, CHING-YU
<u>09803984</u>	Not Issued	161	03/13/2001	Method for fabricating a capacitor by using self-aligned etaching process	CHANG, CHING-YU
<u>09813051</u>	Not Issued	161	03/20/2001	Method of forming contact holes of reduced dimensions by using reverse-transcription process	CHANG, CHING-YU
<u>09823994</u>	6547647	150	04/03/2001	METHOD OF WAFER RECLAIM	CHANG, CHING-YU
<u>09824855</u>	Not Issued	161	04/04/2001	Method for forming rough surface	CHANG, CHING-YU
<u>09851575</u>	6440842	150	05/10/2001	METHOD OF FORMING A DUAL DAMASCENE STRUCTURE BY PATTERNING A SACRIFICIAL LAYER TO DEFINE THE PLUG PORTIONS OF THE STRUCTURE	CHANG, CHING-YU
<u>09854959</u>	6548384	150	05/14/2001	METHOD FOR PERFORMING LITHOGRAPHIC PROCESS TO A MULTI-LAYERED PHOTORESIST LAYER	CHANG, CHING-YU
<u>09858513</u>	Not Issued	161	05/17/2001	Method of use for reusable monitor wafer	CHANG, CHING-YU
<u>09861623</u>	6632587	150	05/22/2001	METHOD OF ENHANCING PHOTORESIST ANTI-ETCHING ABILITY	CHANG, CHING-YU
<u>09871663</u>	6534407	150	06/04/2001	METHOD FOR REDUCING DISHING EFFECTS DURING A CHEMICAL MECHANICAL POLISHING PROCESS	CHANG, CHING-YU
<u>09871664</u>	6562144	150	06/04/2001	WAFER CLEANING METHOD	CHANG, CHING-YU

<u>09872304</u>	Not Issued	161	06/01/2001	Method for forming a contact window with low resistance	CHANG, CHING-YU
<u>09880229</u>	Not Issued	161	06/12/2001	Development method for manufacturing semiconductors	CHANG, CHING-YU
<u>09885038</u>	<u>6579810</u>	150	06/21/2001	METHOD OF REMOVING A PHOTORESIST LAYER ON A SEMICONDUCTOR WAFER	CHANG, CHING-YU
<u>09903667</u>	<u>6569761</u>	150	07/13/2001	METHOD FOR SHRINKING CRITICAL DIMENSION	CHANG, CHING-YU
<u>09924904</u>	<u>6448605</u>	150	08/08/2001	METHOD OF FABRICATING GATE	CHANG, CHING-YU
<u>09925001</u>	Not Issued	161	08/08/2001	Method of fabricating gate	CHANG, CHING-YU
<u>09939982</u>	<u>6541828</u>	150	08/23/2001	NON-VOLATILE MEMORY DEVICE USED FOR NON-OVERLAPPING IMPLANT	CHANG, CHING-YU
<u>09949693</u>	<u>6787013</u>	150	09/10/2001	NEW SPACER FORMING METHOD USED FOR A BIOSENSOR	CHANG, CHING-YU
<u>09983256</u>	Not Issued	161	10/23/2001	Apparatus and method for preventing the re-adherence of particles in wafer-cleaning process	CHANG, CHING-YU
<u>09990452</u>	Not Issued	161	11/20/2001	Method for improving resolution limits of a stepper	CHANG, CHING-YU
<u>10036362</u>	<u>6638441</u>	150	01/07/2002	METHOD FOR PITCH REDUCTION	CHANG, CHING-YU
<u>10063129</u>	<u>6802322</u>	150	03/25/2002	METHOD OF FABRICATING A STRINGERLESS FLASH MEMORY	CHANG, CHING-YU
<u>10063199</u>	<u>6713388</u>	150	03/28/2002	METHOD OF FABRICATING A NON-VOLATILE MEMORY DEVICE TO ELIMINATE CHARGE LOSS	CHANG, CHING-YU
<u>10065003</u>	<u>6861176</u>	150	09/09/2002	HOLE FORMING BY CROSS-SHAPE IMAGE EXPOSURE	CHANG, CHING-YU
<u>10065431</u>	<u>6720210</u>	150	10/17/2002	MASK ROM STRUCTURE AND MANUFACTURING METHOD THEREOF	CHANG, CHING-YU
<u>10065644</u>	Not Issued	161	11/05/2002	Method for forming opening and application thereof	CHANG, CHING-YU

[Search and Display More Records.](#)

Search Another: Inventor	Last Name	First Name	<input type="button" value="Search"/>
	<input type="text" value="CHANG"/>	<input type="text" value="CHING-YU"/>	

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)